IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hisanori FUJISAWA

Application No.: 09/045,041

Confirmation No.: 9340

Filed: March 20, 1998

U.S. Patent No.: 6,999,911

Issued: February 14, 2006

For: METHOD AND APPARATUS FOR CARRYING OUT CIRCUIT SIMULATION

ATTENTION

CERTIFICATE OF CORRECTION

BRANCH

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Certificate

MAY 1 8 2006

of Correction

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 USC § 254 and 37 CFR § 1.322, to correct the error(s) shown on the attached Form PTO-1050. A check in the amount of $100 to cover the cost of the Certificate is attached.

One or more of the errors shown on the attached Form PTO-1050 is or are within the responsibility of the undersigned; each thereof is of a clerical or typographical nature or of minor character and occurred in good faith, and the correction thereof is consistent with the prosecution record.

05/16/2006 JBALINAN 00000032 6999911
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Date: _May 15, 2006_

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Respectfully submitted,

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MAY 18 2006

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,999,911

DATED : February 14, 2006

INVENTOR(S) : Hisanori FUJISAWA

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Column 17, Line 33, change "a given terminal" to -- one or more terminals --
Column 17, Line 64, delete "circuit extracting" second occurrence

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PATENT NO. 6,999,911

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